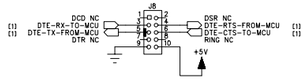
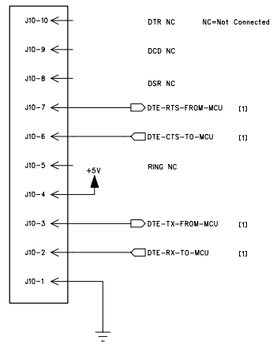
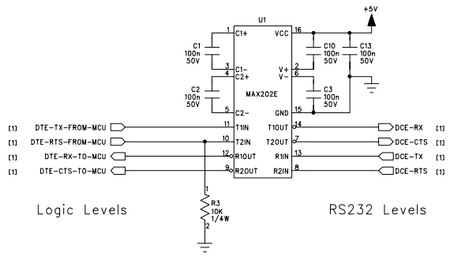
Not Installed
Optional Connector/Test Points

UART Logic Level 2x5 Header
(DTE Signal Names on Header, TX is an output)
UART Module with 2x5 DIL Header



UART Logic Level 1x10 Header
Header, 0.1" Sp, 10 pin, Vertical



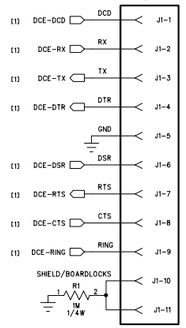


Note the terminology change from DCE to DTE signal names as they pass through the transceiver.
For example:
The data comes in on DCE-TX at RS232 levels.
It switches to DTE naming when converted to logic level and the signal name becomes DTE-RX-TO-MCU.

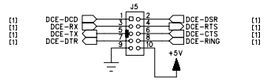
When connecting two RS232 modules, the RS232 signals of the same name are connected.
i.e. DTE-RX connects to DCE-RX.

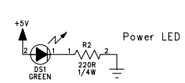
This allows the DTE and the DCE modules to have the same pins names on the logic level side.

RS232 DCE Connector
(TX is an input to ComBoard)
DB9 Female, RA



RS232 DCE Header
(TX is an input to ComBoard)
2x5 DIL Header





REV.	DATE	ECO NUMBER (DESCRIPTION)	APPR'D
1.00	31Aug10	Released	SK
1.01	29Aug11	Signal name changes.	SK

DESIGNED Scot Kornak	DATE Jun 2010
DRAWN Scot Kornak	DATE Jun 2010
CHECKED Scot Kornak	DATE Aug 2010
APPROVED Scot Kornak	DATE Aug 2010

BusBoard Prototype Systems Ltd.
www.BusBoard.net

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CLASSIFICATION
RELEASED TO PUBLIC

TITLE
ComBoard
CB232F
RS232-DCE-DB9F

SSI DRAWING NUMBER - REVISION
BPS-SCH-(CB232F) Rev 1.01

DRAWING IS FOR PART NUMBER - REVISION
PCB-7770-1.00

SHEET 1 OF 1

* = Part fitted to incorrect decal. & = Part added as salvage. X = track cut.